

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi KURODA et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any  
assertion as to materiality or prior art effect, the  
document listed on the attached Form PTO-1449 is hereby  
cited.

The document on the attached List was cited in the  
specification, on page 2, and its relevance is indicated  
therein.

Respectfully submitted,

MWS:lat

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By: 

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December 17, 2003

<b>FORM PTO-1449</b>				<b>Atty. Docket No.</b> <b>XA-10006</b>		<b>Appln. No.</b>	
<b><u>LIST OF DOCUMENTS CITED BY APPLICANT</u></b>							
				<b>Applicant</b> <b>Hiroshi KURODA et al.</b>			
				<b>Filing Date</b> <b>HEREWITH</b>		<b>Group</b>	
<b>U.S. PATENT DOCUMENTS</b>							
<b>Examiner Initial</b>		<b>Document Number</b>	<b>Date</b>	<b>Name</b>	<b>Class</b>	<b>Sub-class</b>	<b>Filing Date</b>
	AA						
	AB						
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<b>FOREIGN PATENT DOCUMENTS</b>							
<b>Examiner Initial</b>		<b>Document Number</b>	<b>Date</b>	<b>Country</b>	<b>Class</b>	<b>Sub-class</b>	<b>Translation</b>
	AJ	2001-291821	10/19/01	Japan			<b>Abstract</b>
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<b>OTHER</b> (including author, title, date, pertinent pages, etc.)							
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<b>XAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							